

Contact:
Joe McCarthy
Sr. Director, CorpCom
(408) 943-2902

For Immediate Release

CYPRESS LAUNCHES INTEGRATED DUAL-PORT-PCI BUS CONTROLLER
Specialty Memory Leader Integrates PCI Bridge, Memory, and Glue Logic Functions
To Increase Performance and Simplify Bus Design In High-Performance Networking Applications

SAN JOSE, Calif., June 28, 2000 -- Cypress Semiconductor Corp. (NYSE:CY) today introduced a dual-ported memory that serves as a PCI controller, offering a flexible, easy-to-configure interface to a variety of popular processors, including those manufactured by Texas Instruments, Intel, and Motorola. The new chip integrates PCI bridge chip, memory chip and glue-logic functions, offering a simple, cost-effective, single-chip solution that reduces system cost, saves board space, and increases performance.

Compatible with the 33 MHz, 32-bit-wide PCI 2.2 standard for speed and bus width, the CY7C09449PV PCI-Dual Port (PCI-DP™) provides 128K (4K x 32) of high-performance dual-port memory, enabling designers to transfer data from the local processor to the PCI bus in two steps, reducing latency versus the three-step process of moving data from the processor first to external memory, then to the PCI controller, then to the PCI bus. The new chip's high level of integration meets the high-performance standards of high-speed networking applications such as switches, routers, DSLAMs and cable-modems while bringing a true dual-ported solution within reach of more cost-sensitive applications such as low-end mass storage and high-end computation.

“Our single-chip PCI-DP solution leverages Cypress's technological leadership in multiport memories to meet the needs of an exceptionally broad array of networking, storage, and high-end computation customers,” said Jim Dillon, product marketing manager for multi-port memories. “We are targeting the critical mass of the market with 128K of dual-ported memory and the 33/32 PCI standard. Our intent is to seed the market for a higher-density, higher-speed device roadmap.”

“On the communications side, designers benefit from the high performance and reduced latency of a space-efficient, easy-to-use, dual-ported memory,” Dillon said. “In high-end computation systems, such as video boards, interface cards, and MPEG applications, designers who are typically forced to settle for low-end DRAM and SRAM solutions buy up to a dual port's superior performance.”

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The dual-port-PCI Bus controller also provides a configurable, 50 MHz local interface that easily adapts to most processors and can function as a host bridge in an embedded processor application. It can be connected directly to 8-, 16-, and 32-bit processors without glue logic. The chip's PCI bus interface supports both target and bus master modes. It can become a PCI bus master to move any data block, using the full-burst capabilities of the PCI bus. The chip also includes an I2O message passing unit with interrupt capabilities, enabling the local processor to retrieve and post messages to other I2O agents.

The PCI-DP chip is available now in sample quantities, with production in early Q300. The device comes in a 160-pin 24 mm x 24 TQFP with a unit cost of \$18.30 in quantities of 10,000 or more.

About Cypress

Cypress Semiconductor provides high-performance integrated circuit solutions "By Engineers. For Engineers.TM" for fast-growing companies in fast-growing markets, including data communications, telecommunications, computation, consumer products, and industrial-control. With a focus on emerging communications applications, Cypress's product lines include networking-optimized and micropower static RAMs; high-bandwidth multiport and FIFO memories; high-density programmable logic devices; timing technology for PCs and other digital systems; and controllers for Universal Serial Bus (USB). Cypress is No. 1 in the USB and clock chip markets.

More than two-thirds of Cypress's sales come from fast-growing datacom/telecom markets. Cypress's ability to mix and match its broad portfolio of intellectual property enables targeted, integrated solutions for high-speed systems that feed bandwidth-hungry Internet applications. Cypress aims to become the preferred silicon supplier for Internet switching systems and for every Internet data stream to pass through at least one Cypress IC.

Cypress employs more than 3,900 people worldwide with international headquarters in San Jose, California. Its shares are listed on the New York Stock Exchange under the symbol CY. More information about Cypress is accessible electronically on the company's worldwide web site at <http://www.cypress.com> or by CD-ROM (call 1-800-858-1810). An electronic investor forum, and other investor information, is located at <http://www.cypress.com/investor/index.html>.

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